深圳市勋瑞光电科技有限公司

Xunrui Shenzhen Optoelectronics Technology Co., Ltd.

CERT. No. QAC0946535 (ISO9001)

CERT. No. HKG002005 (ISO14001)

Product Specification

Customer: _____

Model Name: H024IQ40E3005

Date: _____

Version: _____

■ **Preliminary Specification**

☐ **Final Specification**

For Customer's Acceptance

Approved by	Comment

Approved by	Reviewed by	Prepared by

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1. Record of Revision

Rev	Issued Date	Description	Editor
1.0	2016/04/29	First Release	Rich Liang
2.0	2017/07/11	Update FPC	Rich Liang

2. General Specifications

Feature		Spec
Characteristics	Size	2.4inch
	Resolution	240(horizontal)*320(Vertical)
	Glass Maker	Tianma
	Interface	8/16-bit MCU
	Connect type	Connector
	Display Colors	65K
	Technology type	a-Si
	Pixel pitch (mm)	0.153x 0.153
	Pixel Configuration	R.G.B.Stripe
	Display Mode	Normally White
	Driver IC	ILI9341V
	Viewing Direction	6 O'clock
	Gray Scale Inversion Direction	12 O'clock
Mechanical	LCM (W x H x D) (mm)	42.72*60.26*2.3
	Active Area(mm)	36.72*48.96
	With /Without TSP	Without
	Weight (g)	10 g
	LED Numbers	4LEDs

Note 1: Viewing direction for best image quality is different from TFT definition; there is a 180 degree shift.

Note 2: Requirements on Environmental Protection: RoHS

Note 3: LCM weight tolerance: +/- 5%

3. Input/output Terminals

PIN NO	PIN NAME	DESCRIPTION
1	IM0	8-/16-bit parallel system interface for 8080 Ⅰ
2	NC	-
3	NC	-
4	NC	-
5	NC	-
6	GND	Ground
7	IOVCC	Low voltage power supply for interface logic
8	VCI	power supply
9	TE	Tearing effect output pin to synchronize MPU to
10	/CS	Chip select
11	RS	This pin is used to select “Data or Command” in
12	/WR	Write signal
13	/RD	Read signal
14	DB1	DATA BUS IM0=0 16bit DB1~DB9,DB10~DB17 IM0=1 8bit DB10~DB17
15	DB2	
16	DB3	
17	DB4	
18	DB5	
19	DB6	
20	DB7	
21	DB8	
22	DB10	
23	DB11	
24	DB12	
25	DB13	
26	DB14	
27	DB15	
28	DB16	
29	DB17	
30	RESET	Reset signal input terminal, active at ‘L'
31	NC	-
32	LEDA	LED backlight anode
33	LEDK1	LED backlight cathode
34	LEDK2	
35	LEDK3	
36	LEDK4	

37	NC	
38~40	GND	Ground

4. Absolute Maximum Ratings

Item	Symbol	MIN	MAX	Unit	Remark
Supply Voltage	V_{CC}	2.5	4.8	V	
Input Voltage	IOVCC	1.65	3.3	V	
Operating Temperature	T_{OPR}	-20	70	℃	
Storage Temperature	T_{STG}	-30	80	℃	

5. Electrical Characteristics

Driving TFT LCD Panel

Ta = 25 °C

Item		Symbol	MIN	TYP	MAX	Unit	Remark
Analog Supply Voltage		V_{CC}	2.5	-	3.3	V	
Logic Signal Input /Output Voltage		IOVCC	1.65		3.3	V	
Input Signal Voltage	Low Level	V_{IL}	VSS	-	0.3x IOVCC	V	
	High Level	V_{IH}	0.7x IOVCC	-	IOVCC	V	
TFT Common Electrode		V_{COMH}	2.5	-	5	V	
TFT Gate ON Voltage		V_{GH}	10	-	16	V	
TFT Gate ON Voltage		V_{GL}	-10	-	-5	V	

Driving Backlight

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	I_F	-	80	100	mA	
Forward Voltage	V_F	-	3.2	3.3	V	
Backlight Power consumption	W_{BL}	-	0.256	0.33	W	
LED Lifetime		25000	-	-	Hrs	

Note 1: Each LED: IF =20 mA, VF =3.2V.

Note 2: Optical performance should be evaluated at Ta=25℃ only.

Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness.

Typical operating life time is estimated data.

6. Interface Timing

6.1 DC Electrical Characteristics

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
VDDIO	Power supply pin of IO pins	Recommend Operating Voltage Possible Operating Voltage	1.4	-	3.3	V
VCI	Booster Reference Supply Voltage Range	Recommend Operating Voltage Possible Operating Voltage	2.5 or VDDIO whichever is higher	-	3.3	V
VGH	Gate driver High Output Voltage Booster efficiency	No panel loading; 4x or 5x booster; ITO for CYP, CYN, VCIX2, VCI and VCHS = 10 Ohm	88	90	-	%
		No panel loading; 6x booster; ITO for CYP, CYN, VCIX2, VCI and VCHS = 10 Ohm	82	84	-	%
VCIX2	VCIX2 primary booster efficiency	No panel loading, ITO for CYP, CYN, VCIX2, VCI and VCHS = 10 Ohm	83	85	-	%
VGH	Gate driver High Output Voltage		9	-	18	V
VGL	Gate driver Low Output Voltage		-15	-	-6	V
VcomH	Vcom High Output Voltage		V_{CI} + 0.5	-	5	V
VcomL	Vcom Low Output Voltage		$-V_{CIM}$+0.5	-	-1	V
VLCD63	Max. Source Voltage		-	-	6	V
ΔVLCD63	Source voltage variation		-2		2	%
V_{OH1}	Logic High Output Voltage	Iout=-100μA	0.9* VDDIO	-	VDDIO	V
V_{OL1}	Logic Low Output Voltage	Iout=100μA	0	-	0.1*VDDIO	V
V_{IH1}	Logic High Input voltage		0.8*VDDIO	-	VDDIO	V
V_{IL1}	Logic Low Input voltage		0	-	0.2*VDDIO	V
I_{OH}	Logic High Output Current Source	Vout = V_{DDIO}-0.4V	50	-	-	μA
I_{OL}	Logic Low Output Current Drain	Vout = 0.4V	-	-	-50	μA
I_{OZ}	Logic Output Tri-state Current Drain Source		-1	-	1	μA
I_{IL}/I_{IH}	Logic Input Current		-1	-	1	μA

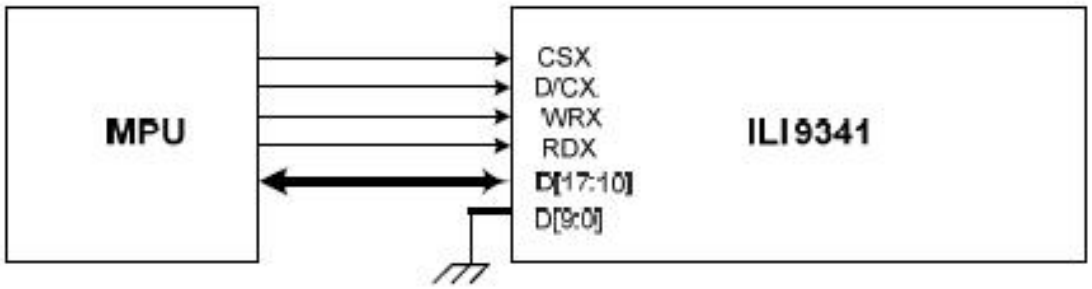
C_{IN}	Logic Pins Input Capacitance			-	5	7.5	pF
R_{SON}	Source drivers output resistance			-	1	-	kΩ
R_{GON}	Gate drivers output resistance			-	5	-	kΩ
R_{COM}	Vcom output resistance			-	200	-	Ω
I_{dp}(262k)	Display current for 262k	Vddio= 1.8V, Vci = 2.8V. 5x/-5x booster ratio. Full color current consumption, without panel loading	Ivdd	-	150	300	uA
			Ivci	-	2.5	8	mA
I_{dp}(8 color)	Display current for 8 color mode	Current consumption for 8 color partial display, without panel loading	Ivdd	-	120	300	μA
			Ivci	-	1	5	mA
I_{slp}	Sleep mode current	Oscillator off, no source/gate output, Ram read write halt. Send command R10-0001 (sleep mode)	Ivdd	-	0.5	1	μA
			Ivci	-	10	75	μA

Remark: Ivdd = Ivddio

Note: The DC characteristic is base on only N-buffer or only P-buffer mode. (Refer to R3Fh command)

6.2 Interface Timing

The 8080- Ⅱ system 8-bit parallel bus interface of ILI9341 can be used by settings as IM [3:0] ="1001". The following shown figure is the example of interface with 8080- Ⅱ MCU system interface.



Different display data formats are available for two color depths supported by listed below.

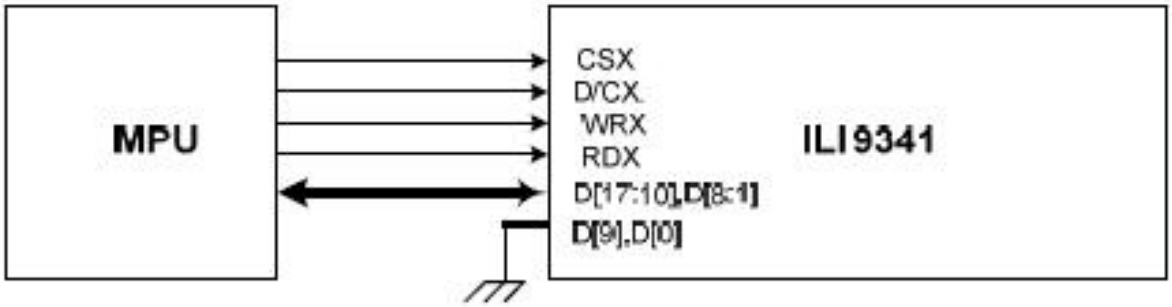
- 65K-Colors, RGB 5, 6, 5 -bits input data.
- 262K-Colors, RGB 6, 6, 6 -bits input data.

65K color: 16-bit/pixel (RGB 5-6-5 bits input)

One pixel (3 sub-pixels) display data is sent by 2 byte transfers when DBI [2:0] bits of 3Ah register are set to "101".

Count	0	1	2	3	4	…	477	478	479	480
D/CX	0	1	1	1	1	…	1	1	1	1
D17	C7	0R4	0G2	1R4	1G2	…	238R4	238G2	239R4	239G2
D16	C6	0R3	0G1	1R3	1G1	…	238R3	238G1	239R3	239G1
D15	C5	0R2	0G0	1R2	1G0	…	238R2	238G0	239R2	239G0
D14	C4	0R1	0B4	1R1	1B4	…	238R1	238B4	239R1	239B4
D13	C3	0R0	0B3	1R0	1B3	…	238R0	238B3	239R0	239B3
D12	C2	0G5	0B2	1G5	1B2	…	238G5	238B2	239G5	239B2
D11	C1	0G4	0B1	1G4	1B1	…	238G4	238B1	239G4	239B1
D10	C0	0G3	0B0	1G3	1B0	…	238G3	238B0	239G3	239B0

The 8080-Ⅱ system 16-bit parallel bus interface of ILI9341 can be selected by settings IM [3:0] ="1000". The following shown figure is the example of interface with 8080-Ⅱ MCU system interface.



Different display data format is available for two colors depth supported by listed below.

65K-Colors, RGB 5, 6, 5 -bits input data.

262K-Colors, RGB 6, 6, 6 -bits input data.

65K color: 16-bit/pixel (RGB 5-6-5 bits input)

One pixel (3 sub-pixels) display data is sent by 1 transfer when DBI [2:0] bits of 3Ah register are set to "101".

Count	0	1	2	3	…	238	239	240
D/CX	0	1	1	1	…	1	1	1
D17		0R4	1R4	2R4	…	237R4	238R4	239R4
D16		0R3	1R3	2R3	…	237R3	238R3	239R3
D15		0R2	1R2	2R2	…	237R2	238R2	239R2
D14		0R1	1R1	2R1	…	237R1	238R1	239R1
D13		0R0	1R0	2R0	…	237R0	238R0	239R0
D12		0G5	1G5	2G5	…	237G5	238G5	239G5
D11		0G4	1G4	2G4	…	237G4	238G4	239G4
D10		0G3	1G3	2G3	…	237G3	238G3	239G3
D8	C7	0G2	1G2	2G2	…	237G2	238G2	239G2
D7	C6	0G1	1G1	2G1	…	237G1	238G1	239G1
D6	C5	0G0	1G0	2G0	…	237G0	238G0	239G0
D5	C4	0B4	1B4	2B4	…	237B4	238B4	239B4
D4	C3	0B3	1B3	2B3	…	237B3	238B3	239B3
D3	C2	0B2	1B2	2B2	…	237B2	238B2	239B2
D2	C1	0B1	1B1	2B1	…	237B1	238B1	239B1
D1	C0	0B0	1B0	2B0	…	237B0	238B0	239B0

7. Optical Characteristics

Items		Symbol	Condition	Min	Typ	Max	Unit	Remark
Viewing angles		θ_T	Center CR≥10	-	65	-	Degree	Note2
		θ_B		-	55	-		
		θ_L		-	65	-		
		θ_R		-	65	-		
Contrast Ratio		CR	Θ =0	300	350	-	-	Note1, Note3
Response Time		T_{ON}	25°C	-	20	30	ms	Note1, Note4
		T_{OFF}		-	25	35		
Chromaticity	White	X_W	Backlight is on	0.26	0.31	0.36	-	Note1, Note5
		Y_W		0.28	0.33	0.38	-	
Uniformity		U		80	-	-	%	Note1, Note6

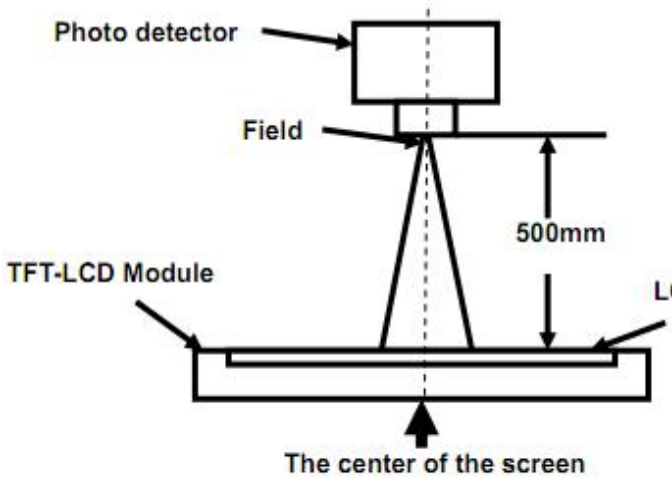
NTSC			-	50	-	%	Note5
Luminance	L		-	300	-	nits	Note1, Note7

Test Conditions:

1. IF= 20mA (one channel), the ambient temperature is 25.
2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

Note 2: Definition of viewing angle range and measurement system.

Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80).

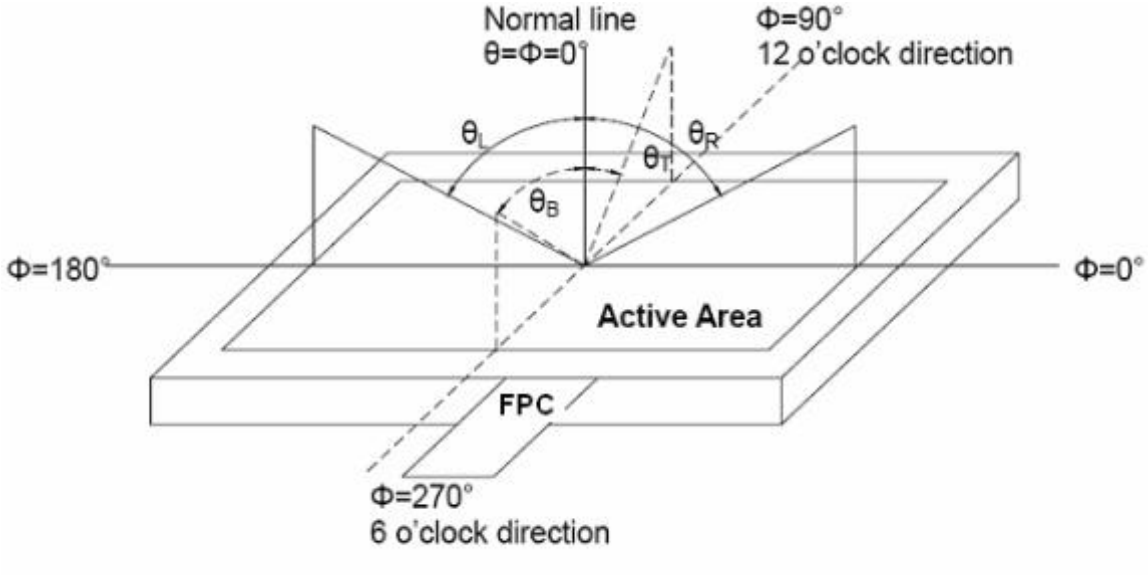


Fig. 1 Definition of viewing angle

Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

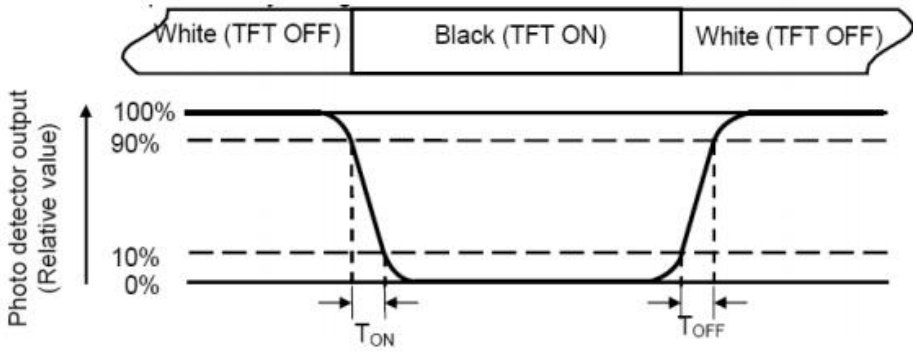
“White state “: The state is that the LCD should drive by Vwhite.

“Black state”: The state is that the LCD should drive by Vblack.

Vwhite: To be determined Vblack: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931).

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = Lmin/ Lmax

L-------Active area length W----- Active area width

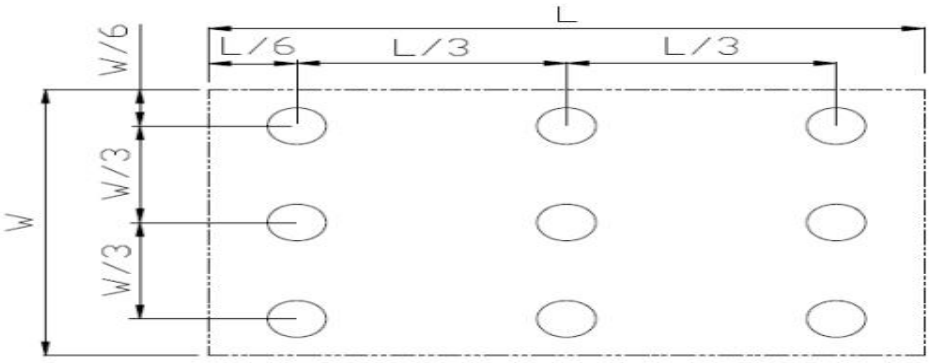


Fig. 2 Definition of uniformity

Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

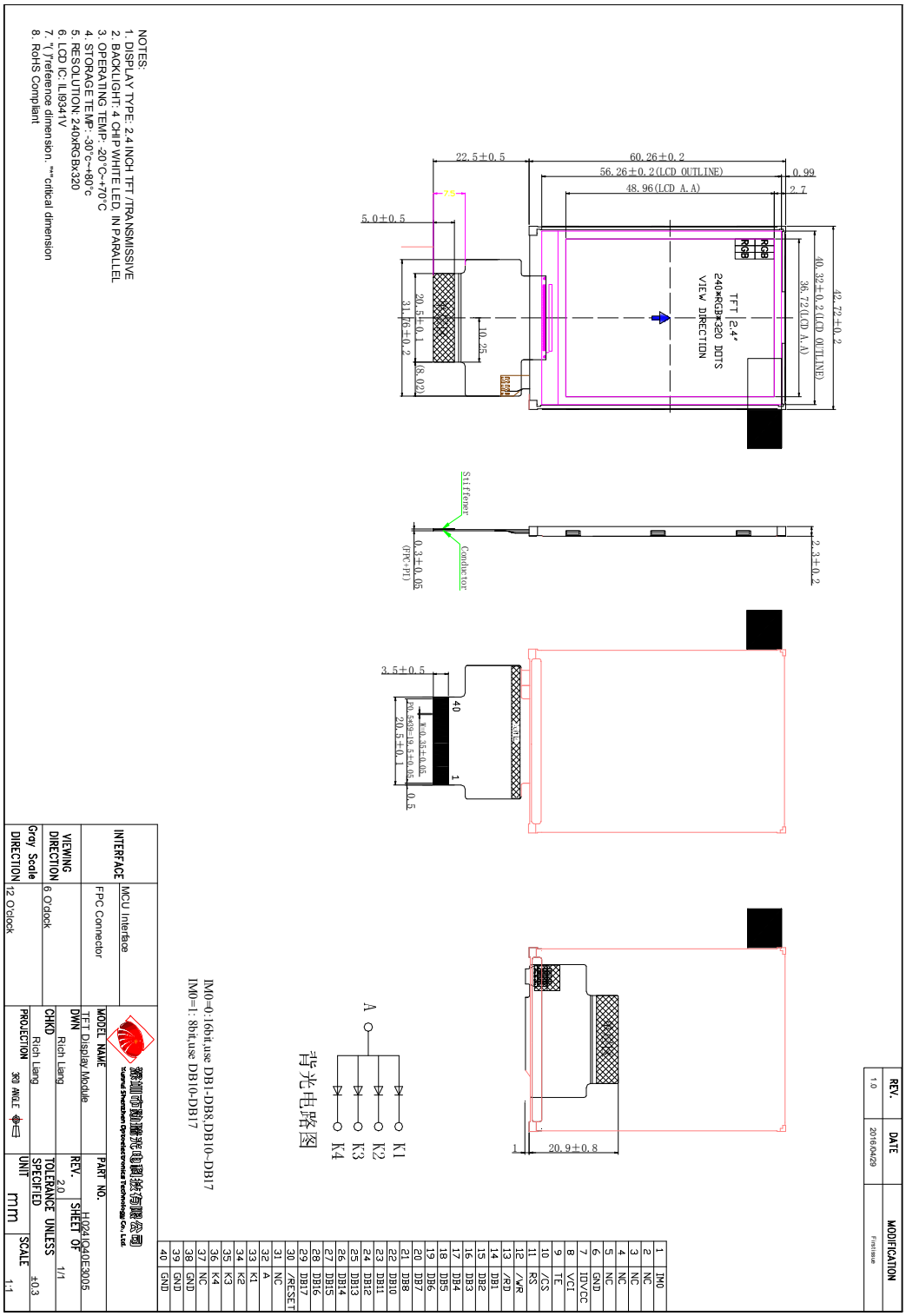
8. Environmental / Reliability Tests

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts= +70℃, 240hrs	Note 1 IEC60068-2-2, GB2423. 2-89
2	Low Temperature Operation	Ta= -20℃, 240hrs	Note 2 IEC60068-2-1 GB2423.1-89
3	High Temperature Storage	Ta= +80℃, 240hrs	IEC60068-2-2 GB2423. 2-89
4	Low Temperature Storage	Ta= -30℃, 240hrs	IEC60068-2-1 GB/T2423.1-89
5	High Temperature & Humidity Storage	Ta= +60℃, 90% RH max, 160 hours	IEC60068-2-3 GB/T2423.3-2006

6	Thermal Shock (Non-operation)	-30℃ 30 min ~ +80℃ 30 min Change time: 5min, 30 Cycle	Start with cold temperature, end with high temperature IEC60068-2-14, GB2423.22-87
7	Electro Static Discharge (Operation)	C=150pF, R=330 Ω, 5 points/panel Air:±8KV, 5 times; Contact: ±4KV, 5times; (Environment: 15℃ ~35℃, 30% ~ 60%, 86Kpa ~ 106Kpa)	IEC61000-4-2 GB/T17626.2-1998
8	Vibration (Non-operation)	Frequency range: 10~55Hz, Stroke: 1.mm Sweep: 10Hz~55Hz~10Hz 2 hours for each direction of X .Y. Z. (package condition)	IEC60068-2-6 GB/T2423.5-1995
9	Shock (Non-operation)	60G 6ms, ± X, ±Y , ± Z,3 times for each direction	IEC60068-2-27 GB/T2423.5-1995
10	Package Drop Test	Height: 80 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8-1995

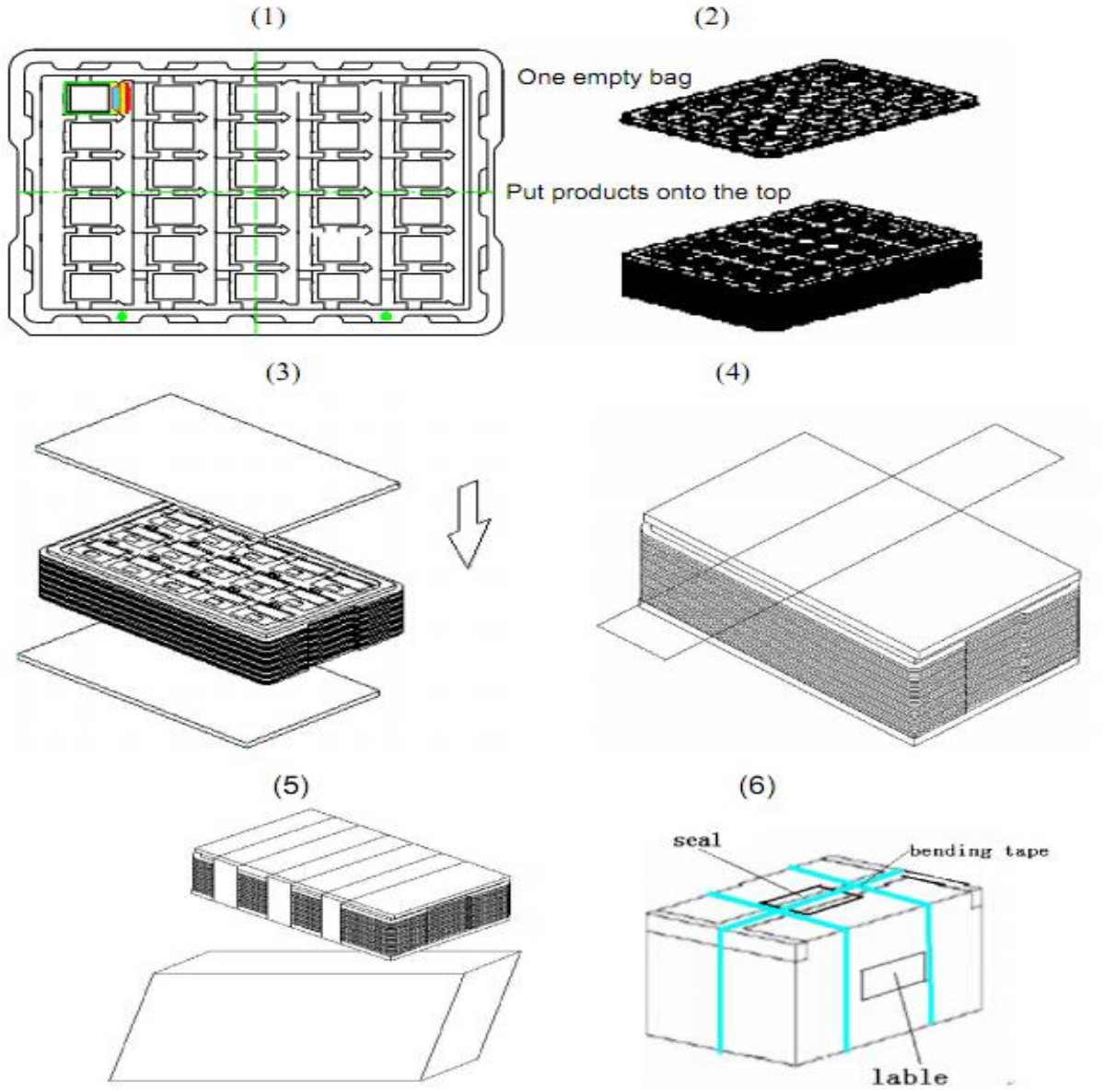
Note: 1. TS is the temperature of panel's surface.
Note: 2. Ta is the ambient temperature of sample.

9. Mechanical Drawing



10. Packing

Packing Method



1. Put module into tray cavity:
2. Tray stacking
3. Put 1 cardboard under the tray stack and 1 cardboard above:
4. Fix the cardboard to the tray stack with adhesive tape:
5. Put the tray stack into carton.
6. Carton sealing with adhesive tape.

11. Precautions for Use of LCD modules

11.1 Handling Precautions

11.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

11.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

11.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

11.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

11.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
-Aromatic solvents

11.1.6 Do not attempt to disassemble the LCD Module.

11.1.7 If the logic circuit power is off, do not apply the input signals.

11.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

11.1.8.1 Be sure to ground the body when handling the LCD Modules.

11.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.

11.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

11.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

11.2 Storage Precautions

11.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

11.2.2 The LCD modules should be stored under the storage temperature range if the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ～ 40℃ Relatively humidity: ≤80%

11.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

11.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.